

FIG. 1

IN-APPARATUS ASYNCHRONOUS RCA (RELAYING TYPE) 40a

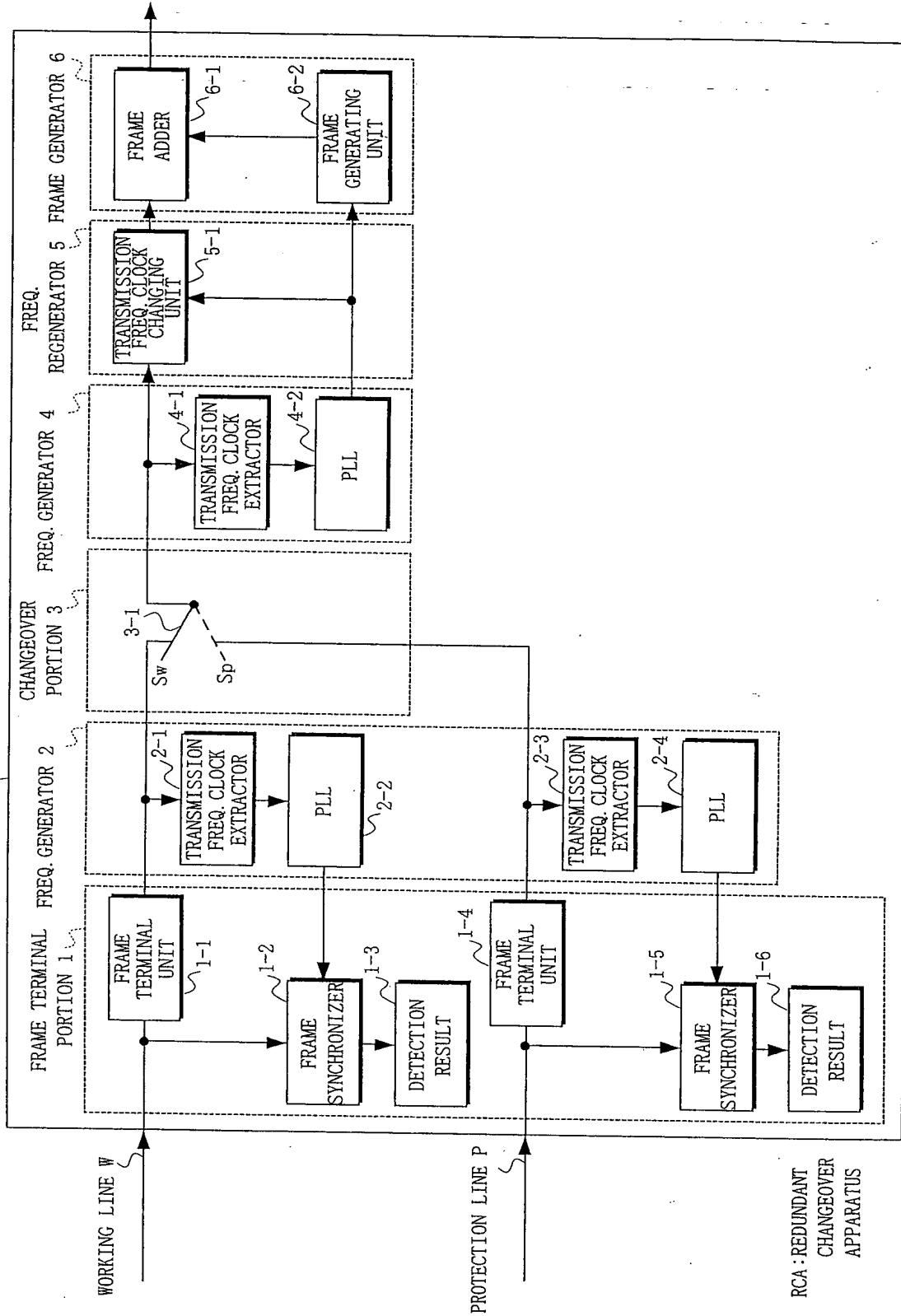


FIG. 2A

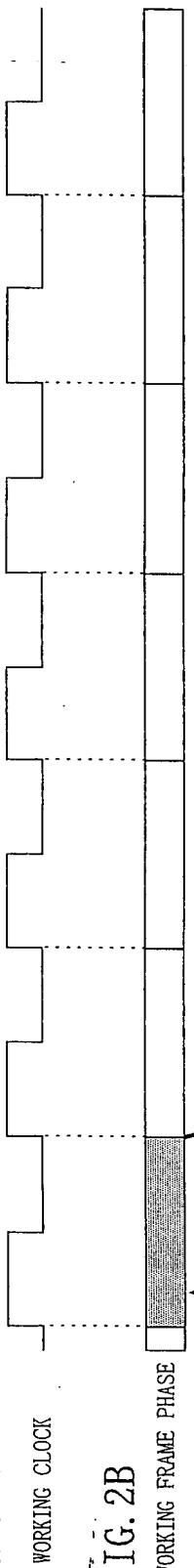


FIG. 2B

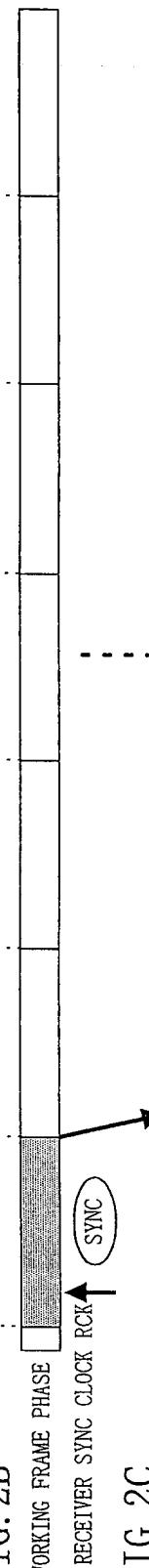


FIG. 2C

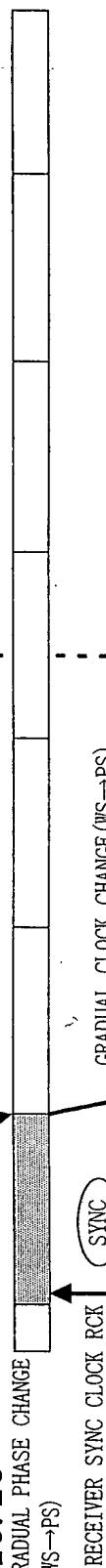


FIG. 2D

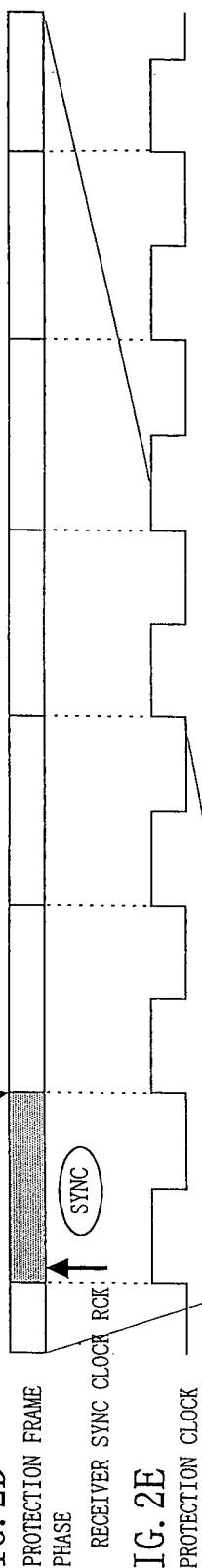


FIG. 2E

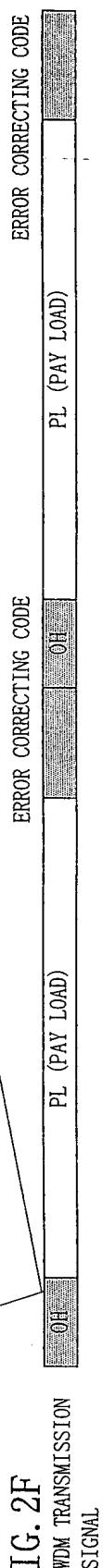


FIG. 2F

WDM TRANSMISSION SIGNAL

FIG. 3

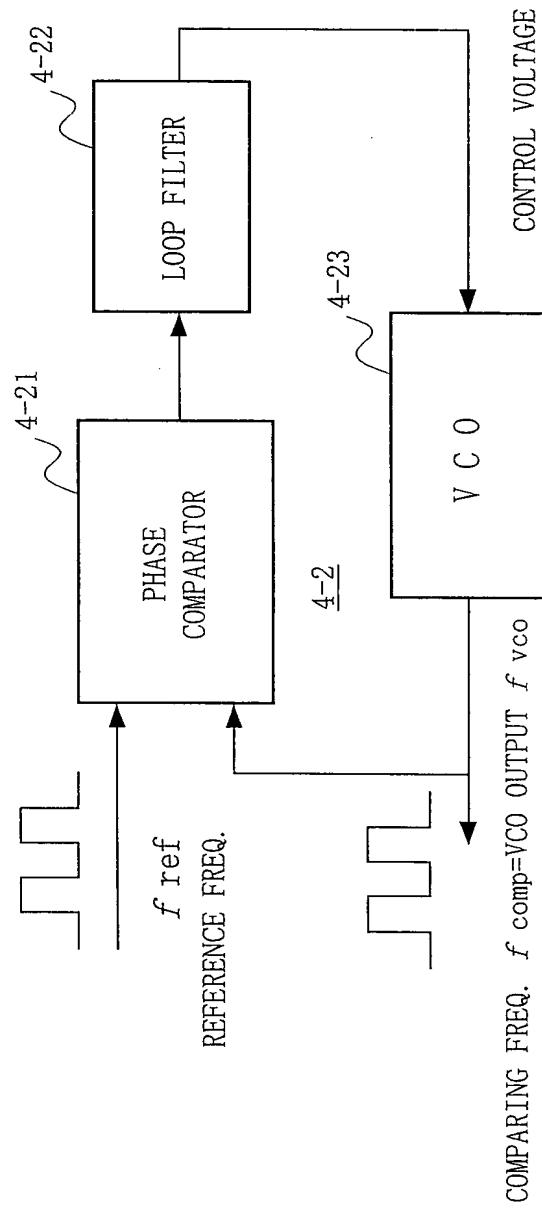


FIG. 4

## IN-APPARATUS ASYNCHRONOUS RCA (DROPPING TYPE) 40b

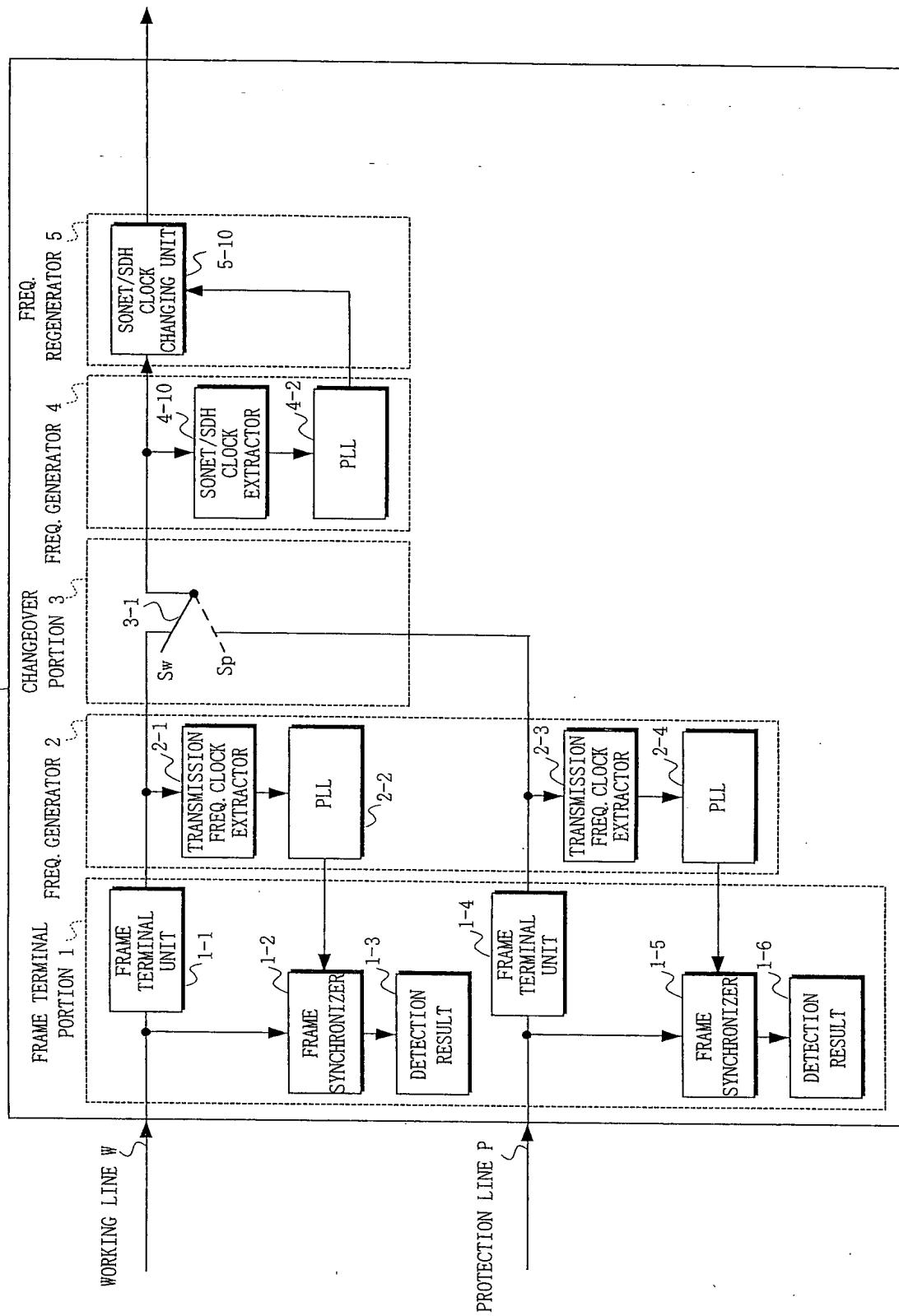


FIG. 5

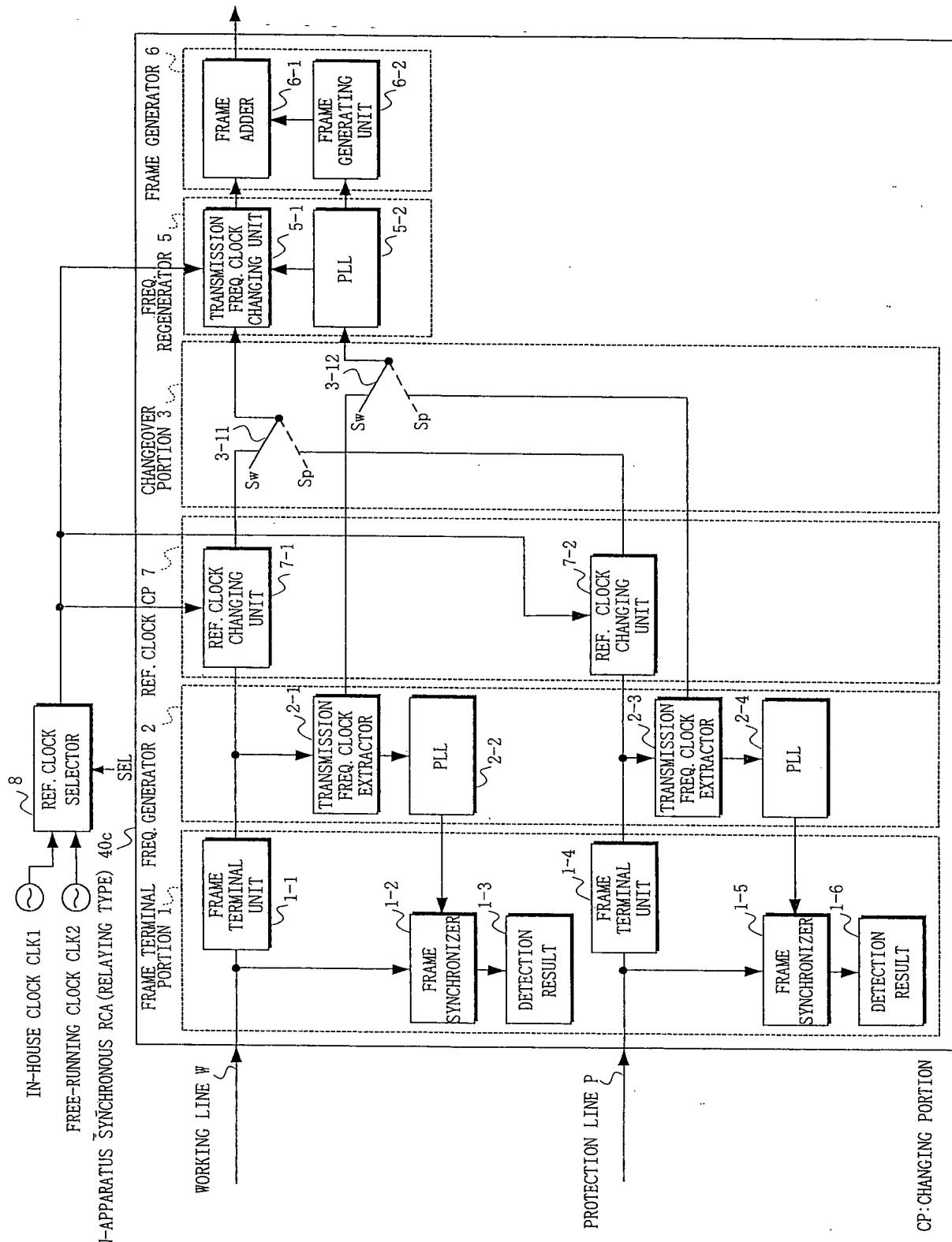


FIG. 6

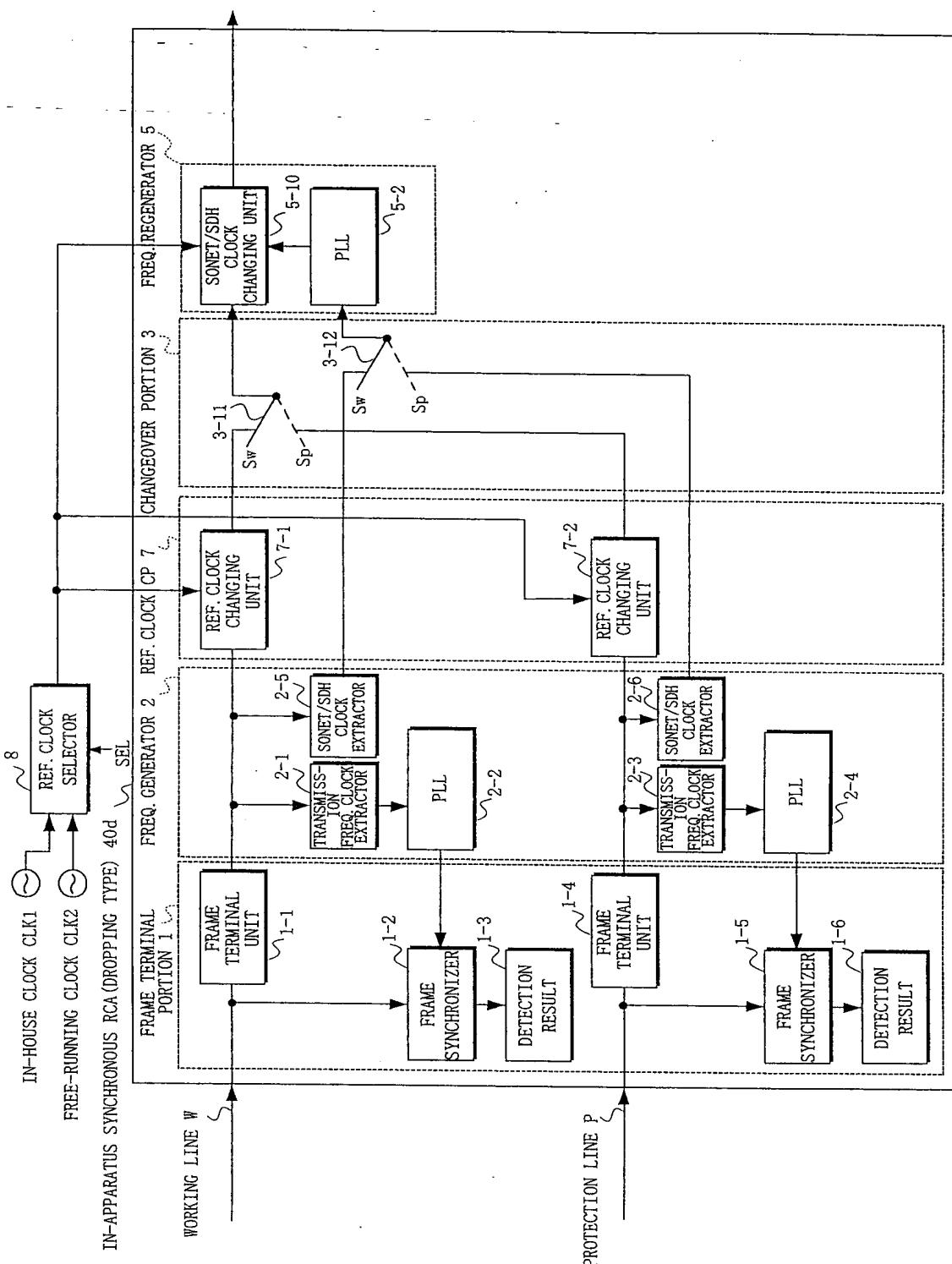


FIG. 7

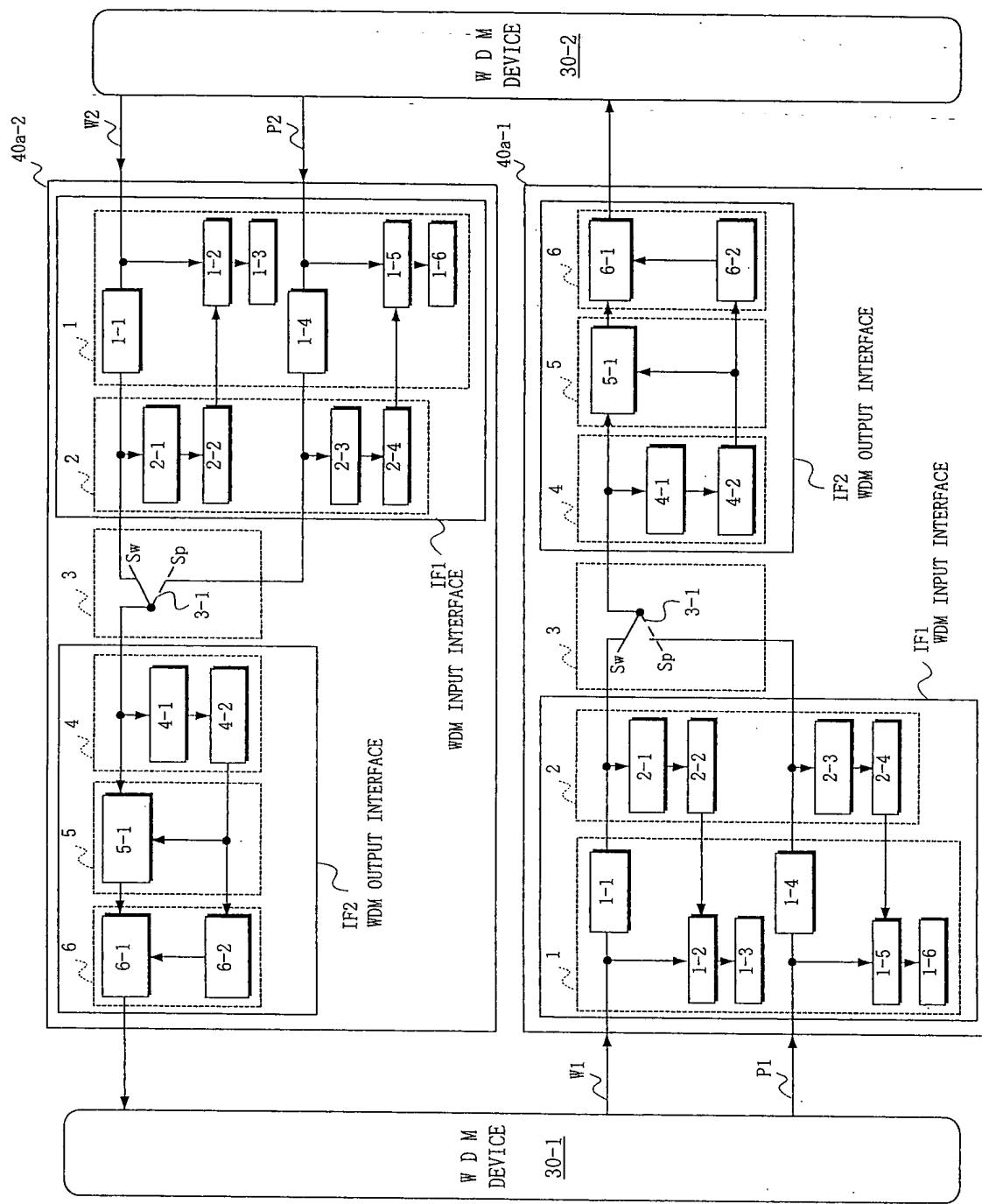


FIG. 8

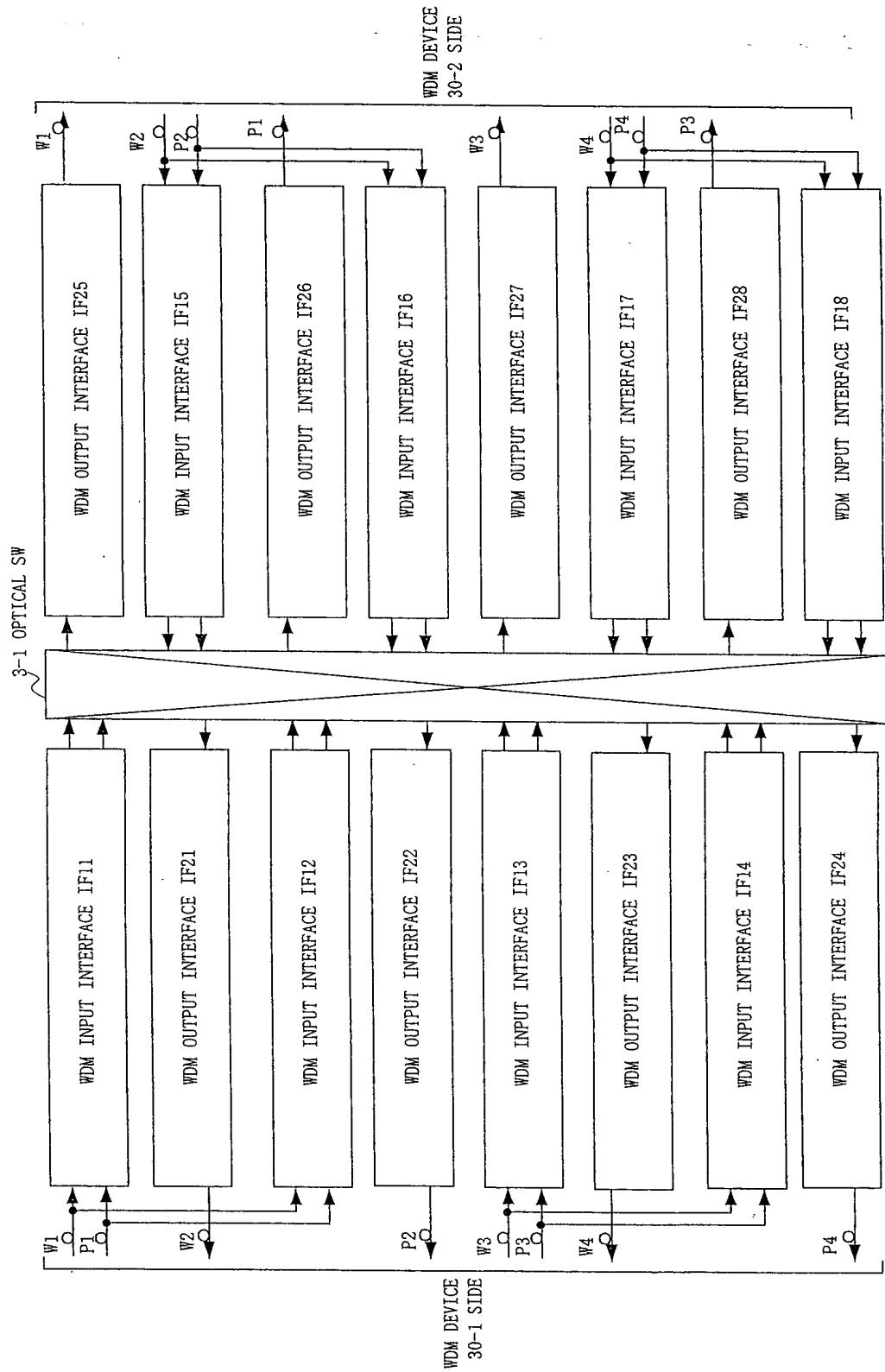


FIG. 9

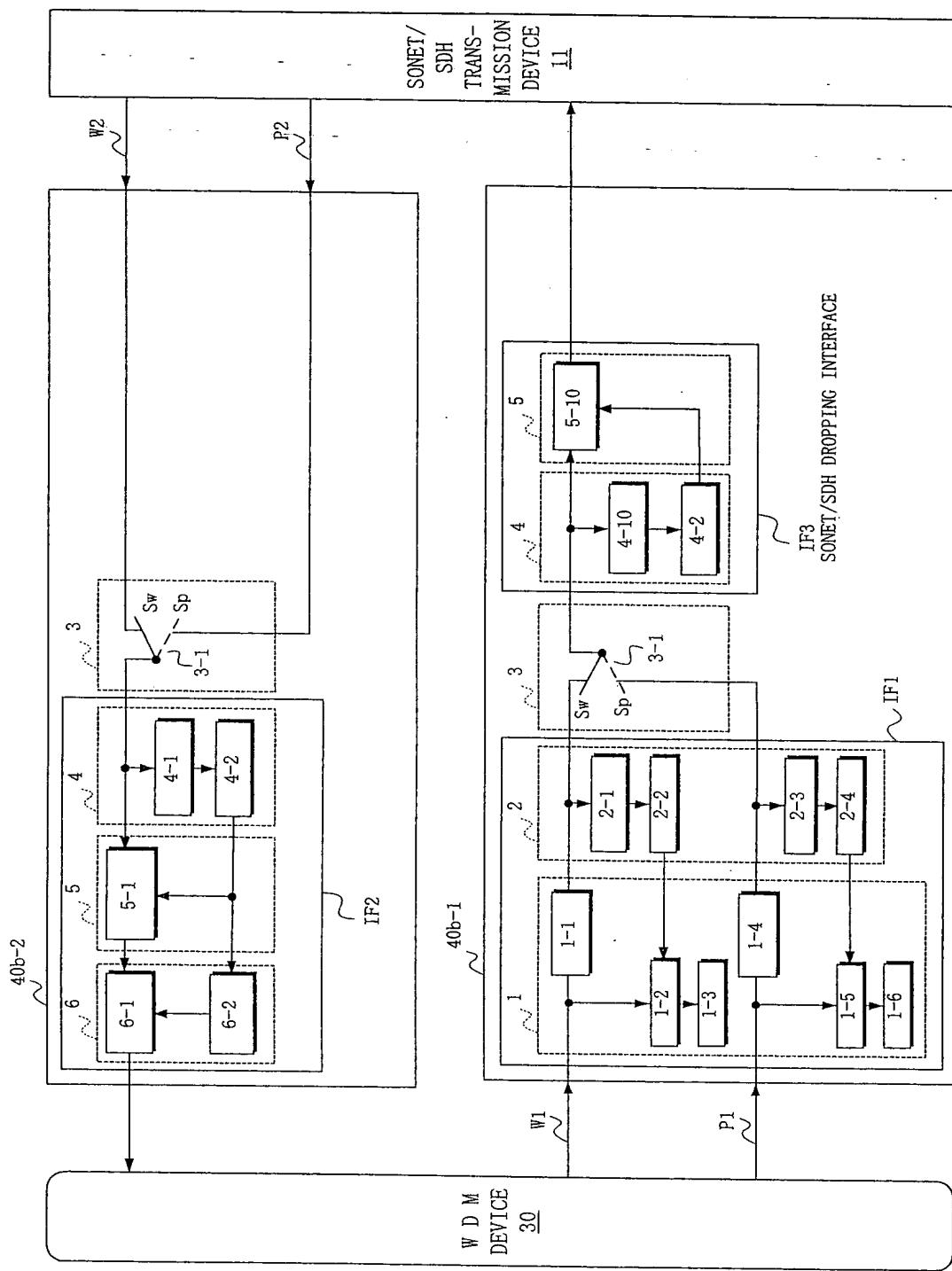
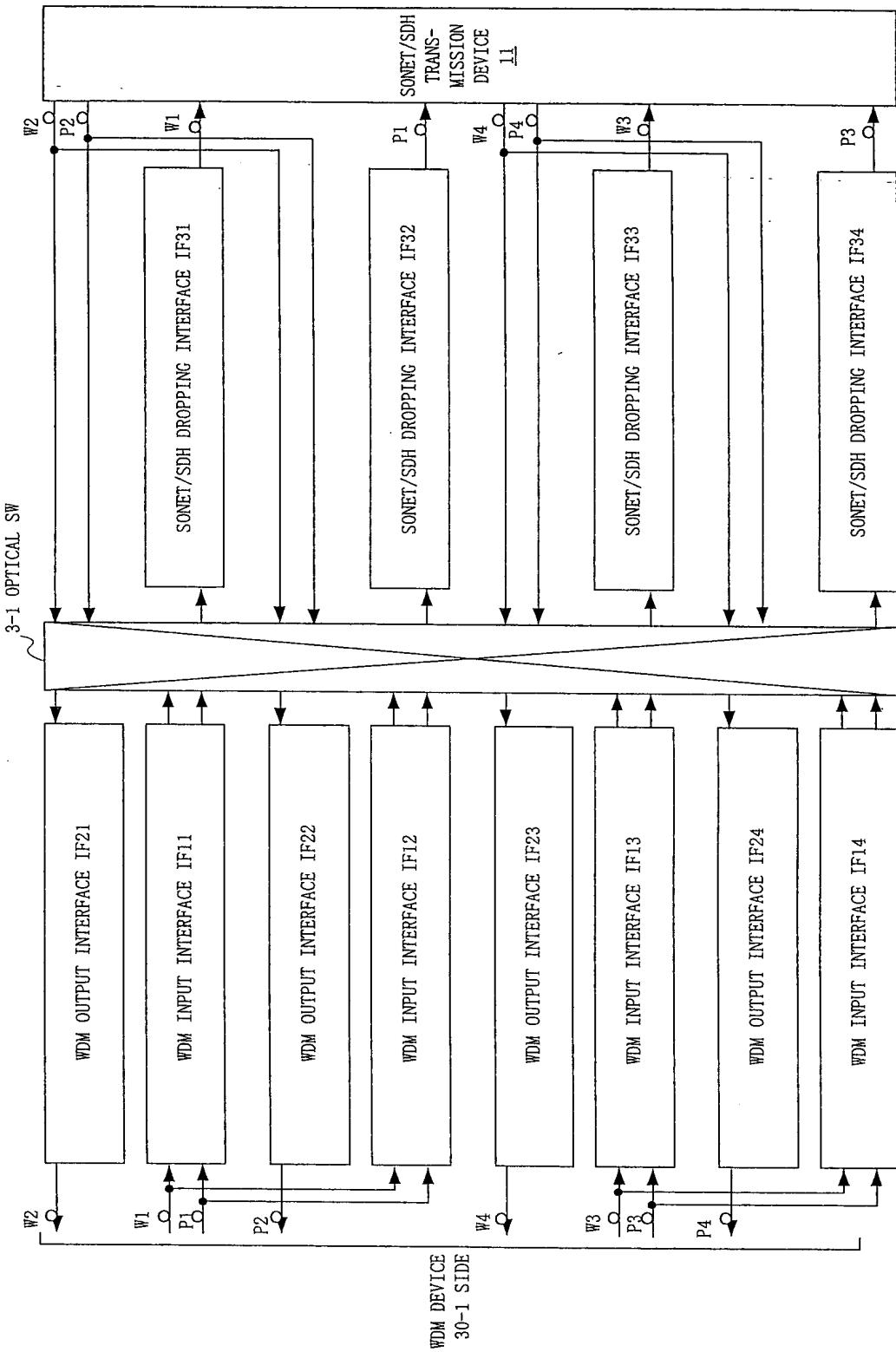


FIG. 10



FG88 ⑪

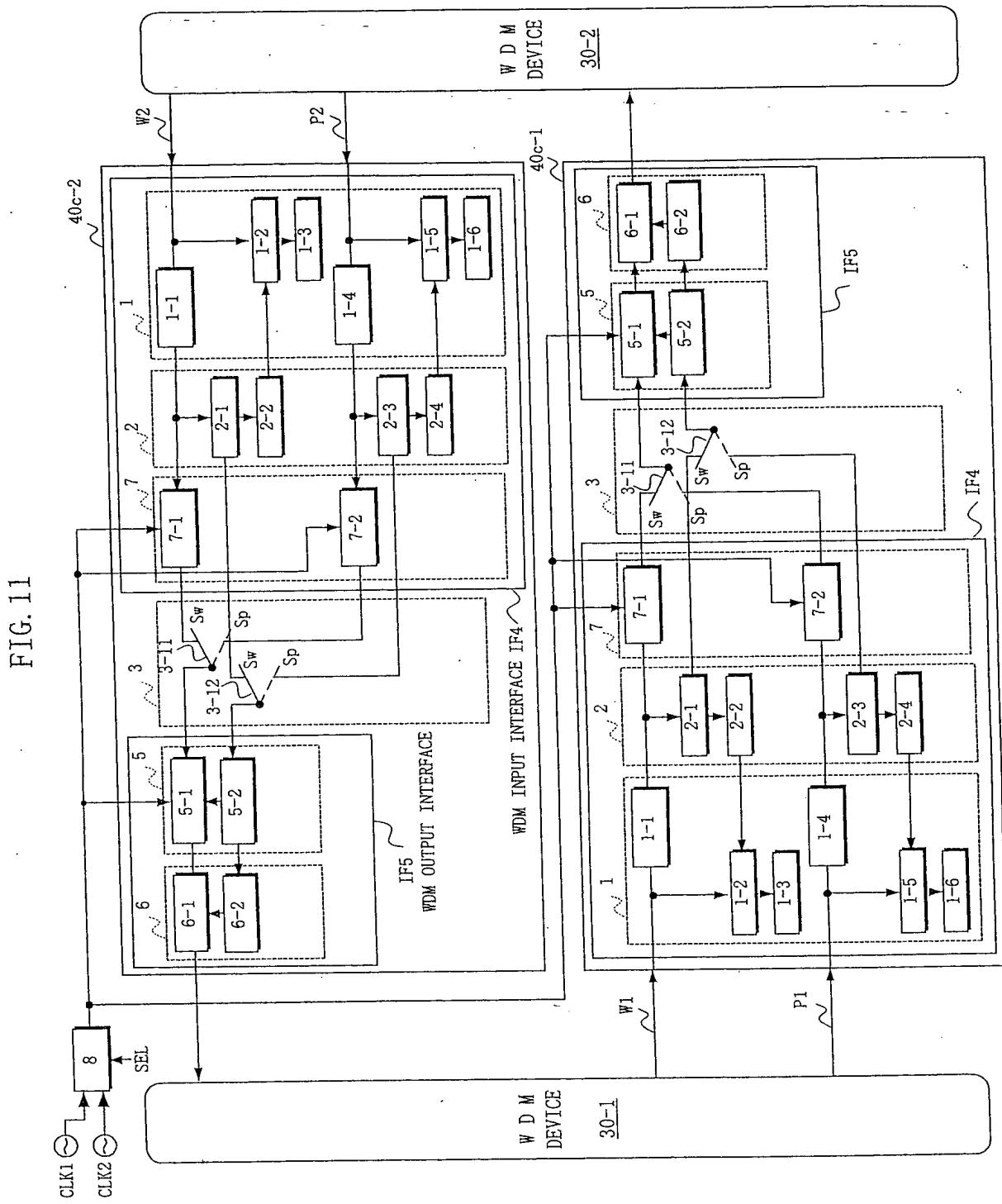
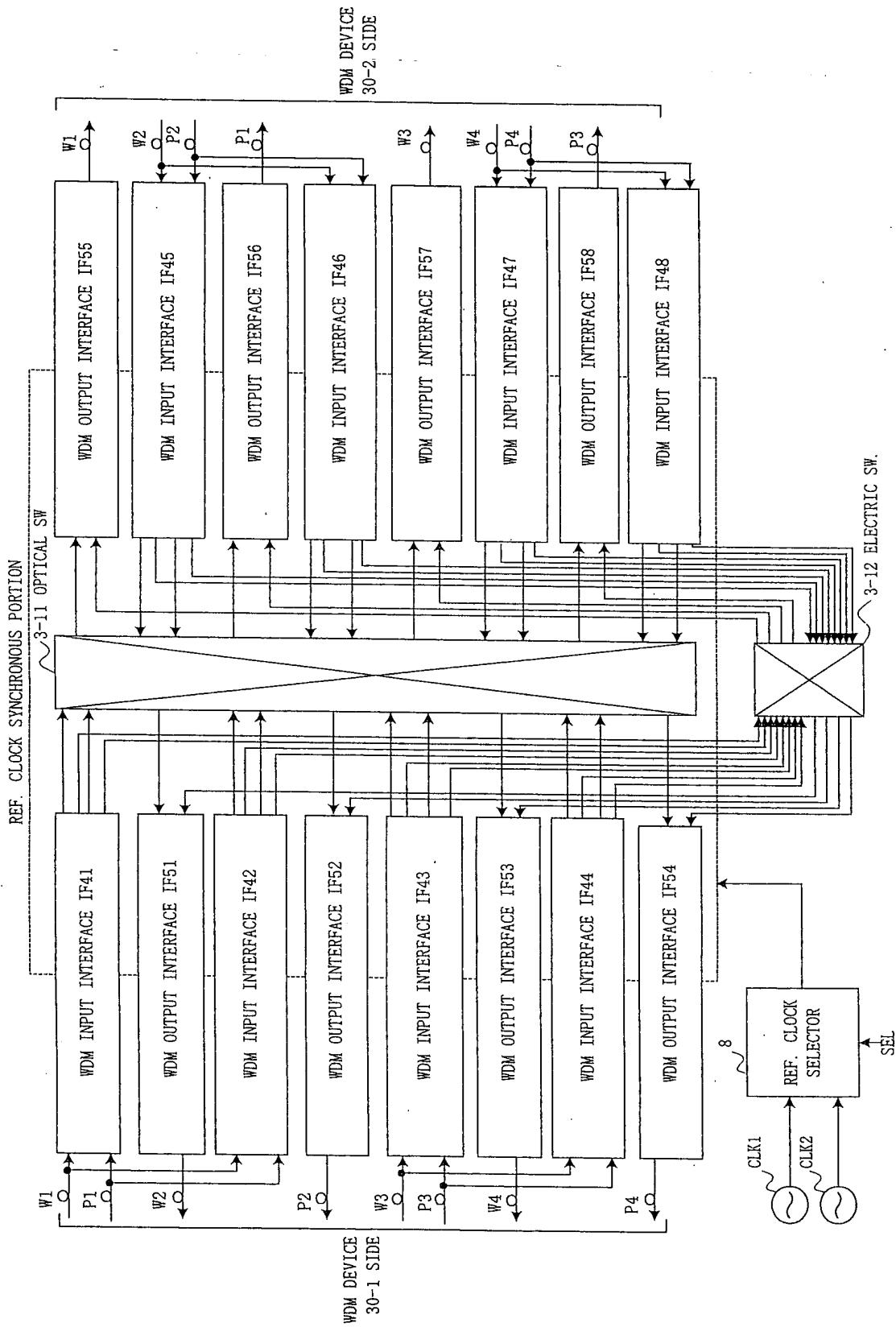


FIG. 12



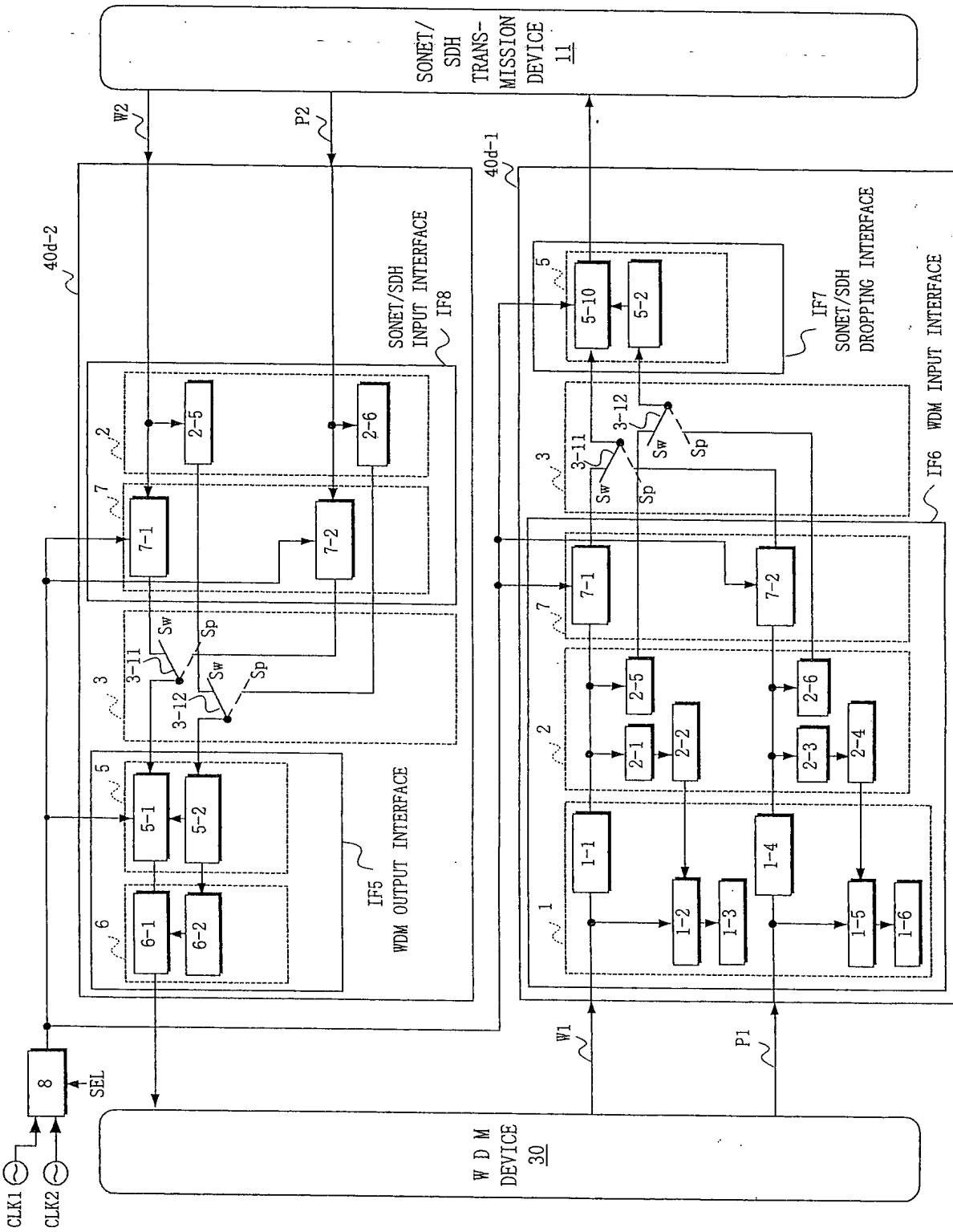


FIG. 14

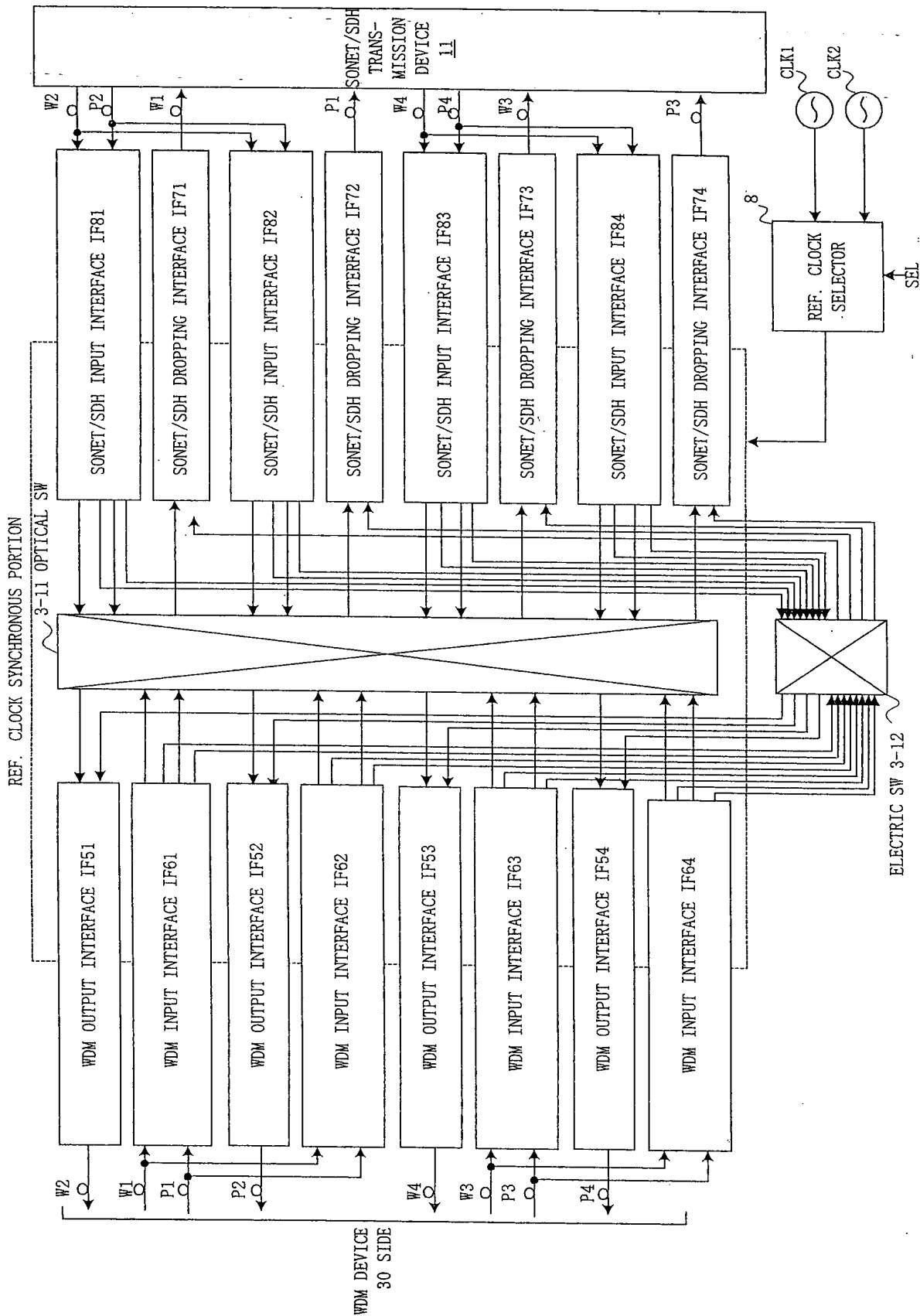


FIG. 15

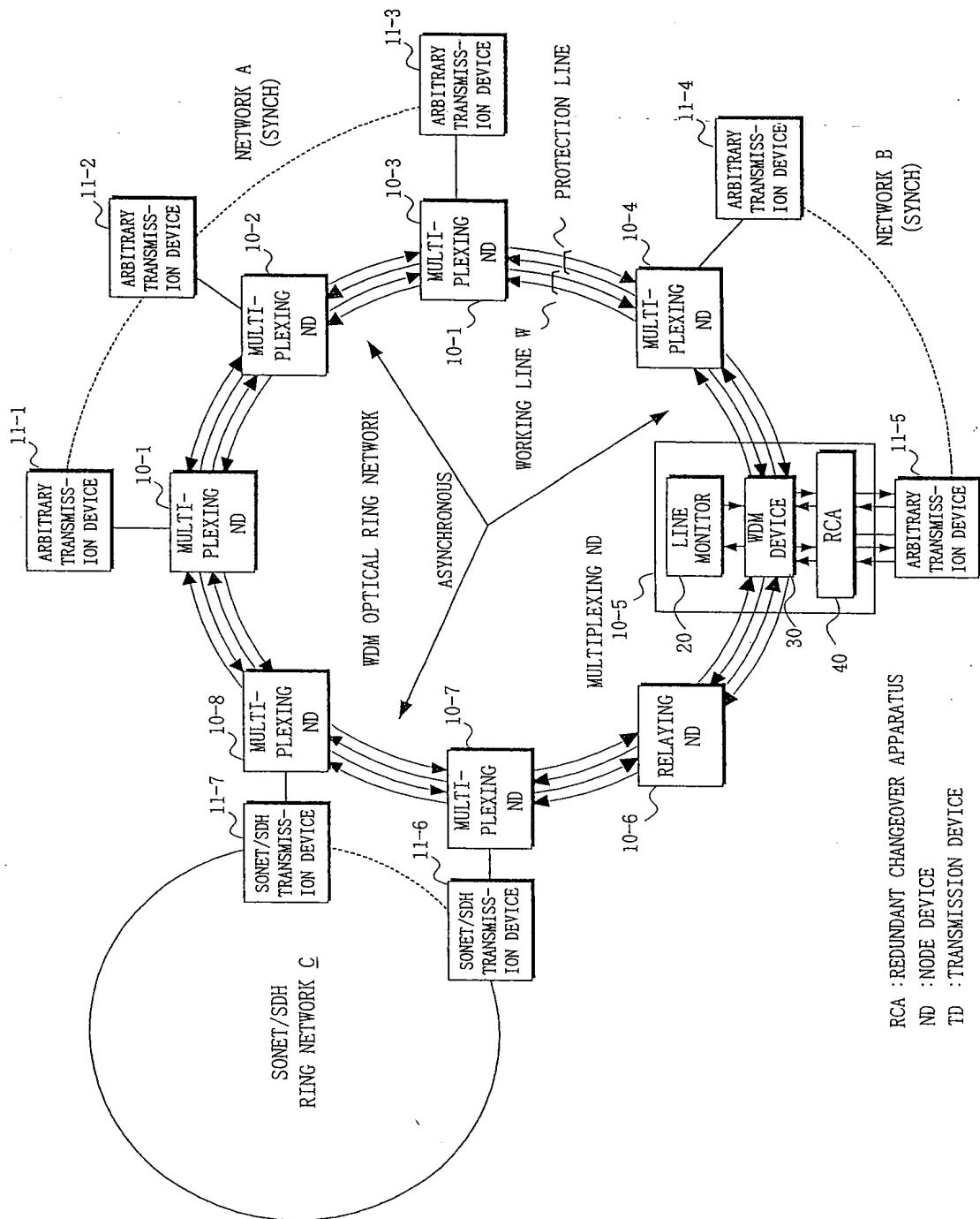


FIG. 16  
PRIOR ART

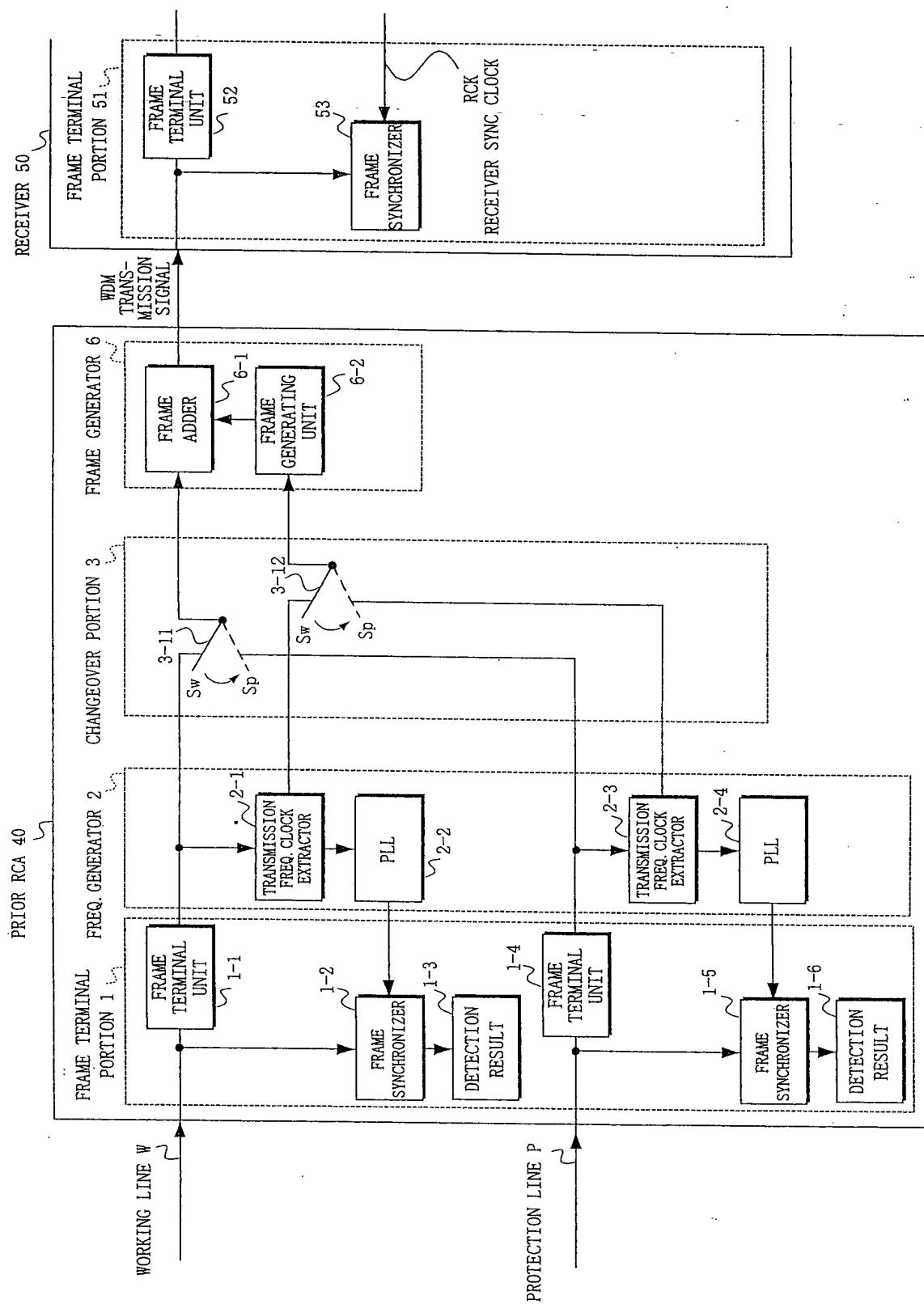


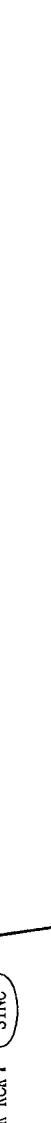
FIG. 17A



FIG. 17B



FIG. 17C



INSTANTANEOUS CLOCK SHIFT (WS→PS)

FIG. 17C



FIG. 17D

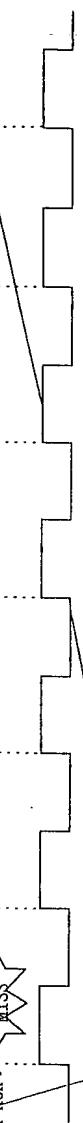


FIG. 17E



ERROR CORRECTING CODE

PL (PAY LOAD)

FIG. 18

